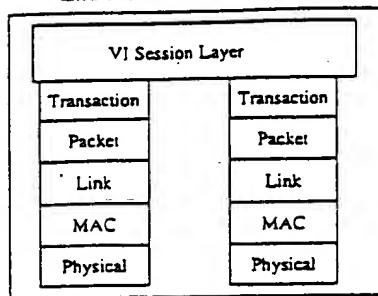


ServerNet II protocol layers

End Node VLA NIC



Routing Node

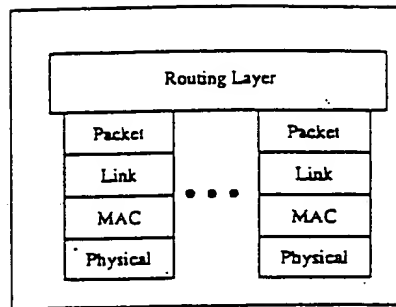


Fig-1

ServerNet II System Area Network

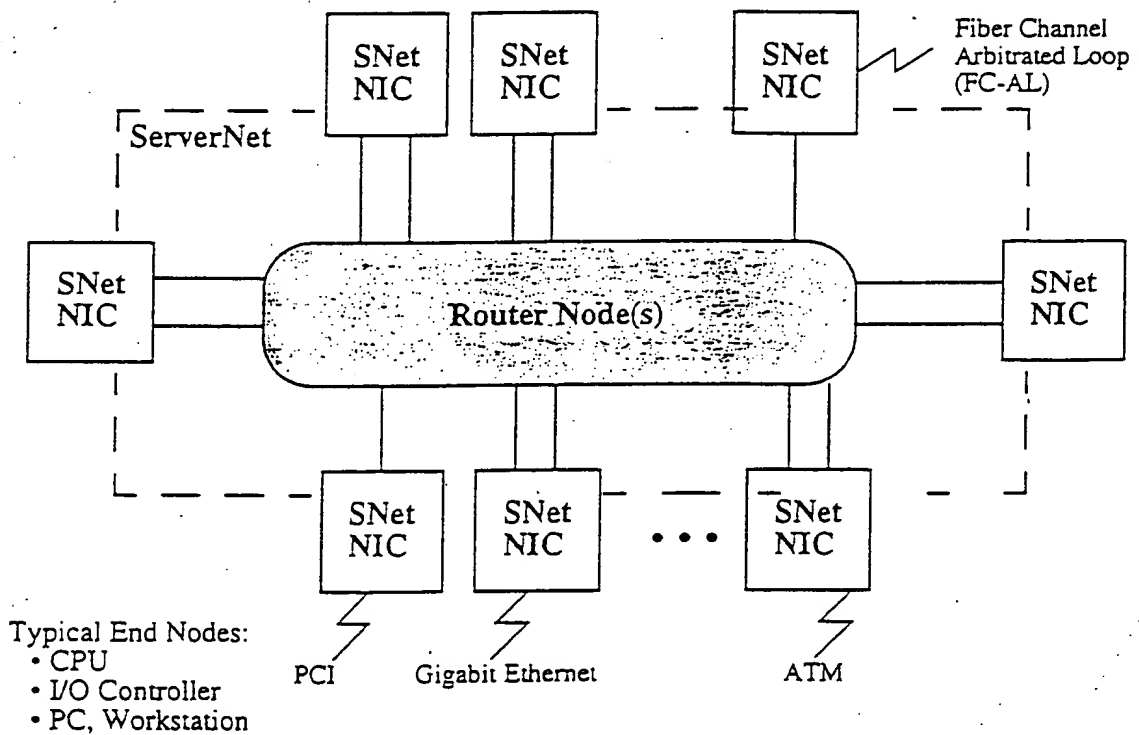
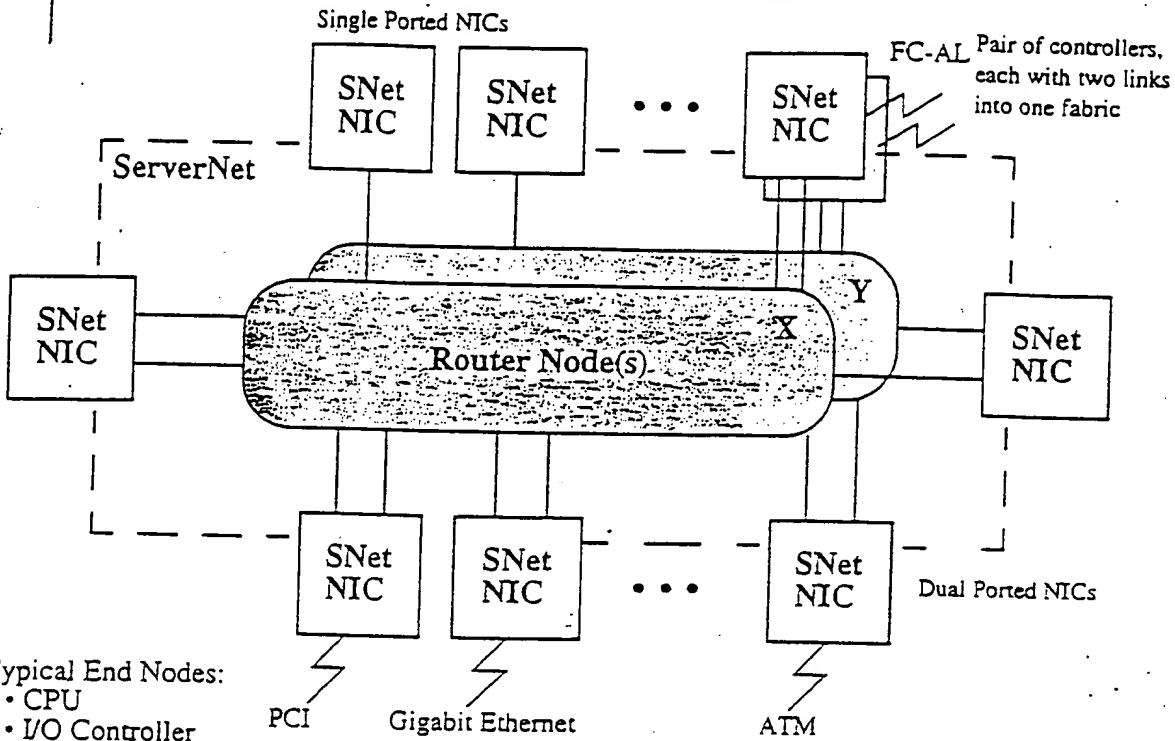


Fig. 2

Fault Tolerant ServerNet II System Area Network



Typical End Nodes:

- CPU
- I/O Controller
- PC, Workstation

Fig. 3

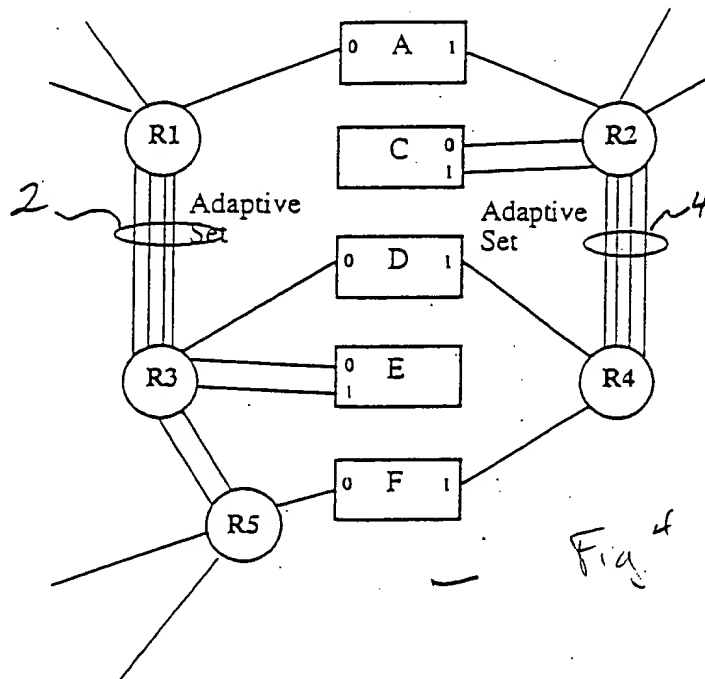
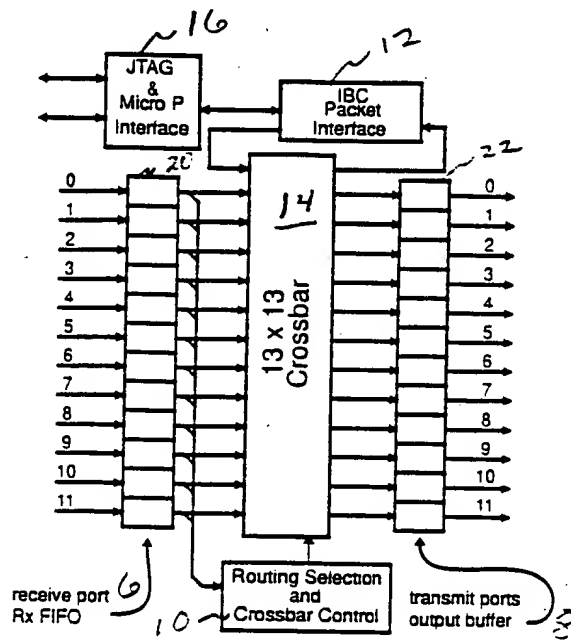


Fig. 4



Router Block Diagram

Fig. 5

Table 2 Physical link translation into Physical lane

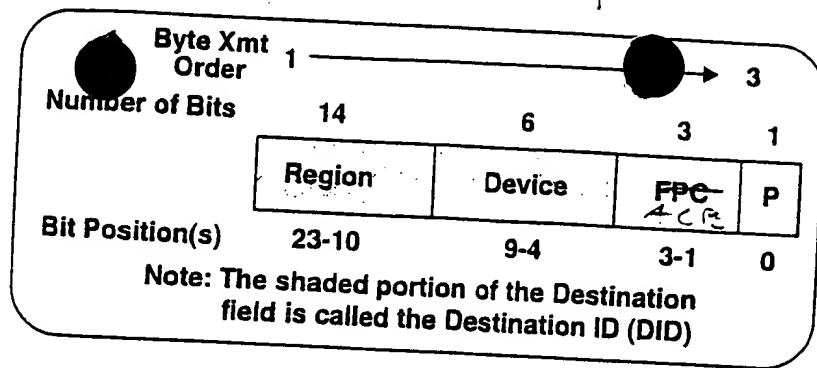
Fat pipe	Physical lane 0	Physical lane 1	Physical lane 2	Physical lane 3
0	1	6	9	
1	5	7	8	11

Fig 6

Destination ID (DID) 20b	ACB 3b	1b	Source ID (SID) 20b	Control 20b
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Figure 7 Packet Header

00000000000000000000000000000000



Destination Field in Packet Header

Fig. 8

Figure 9: Adaptive Control Bits (ACB) Encoding Definition

Encoding	Definition
000	Ordered packet delivery to lane 0 if routed to a port in an Adaptive Set
001	Ordered packet delivery to lane 1 if routed to a port in an Adaptive Set
010	Ordered packet delivery to lane 2 if routed to a port in an Adaptive Set
011	Ordered packet delivery to lane 3 if routed to a port in an Adaptive Set
100	Unordered packet delivery to lane determined by Adaptive Set logic
101 - 111	Reserved (logic in Router-II treats this the same as the 100 encoding)

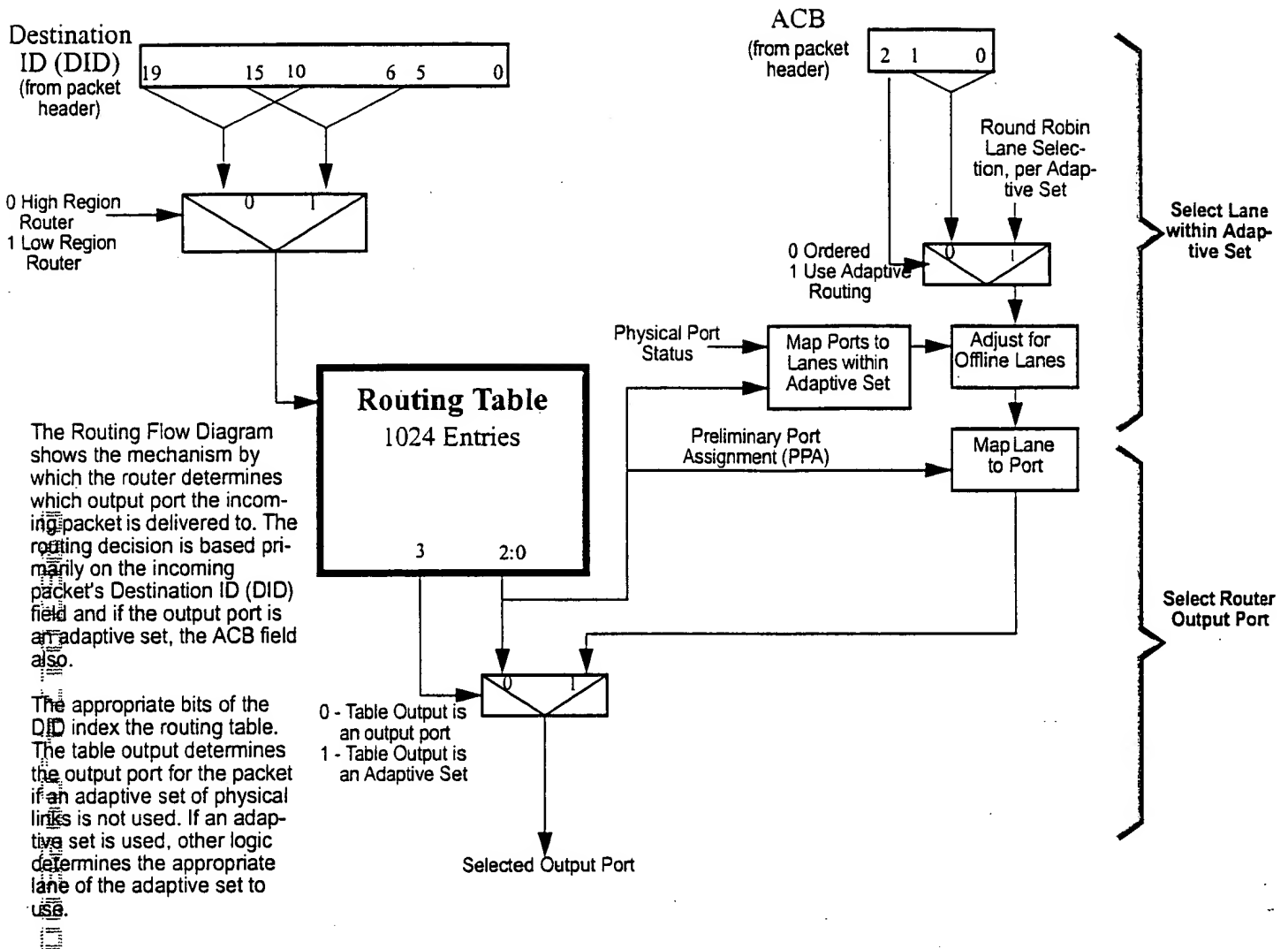


Figure 10. Routing Flow Diagram

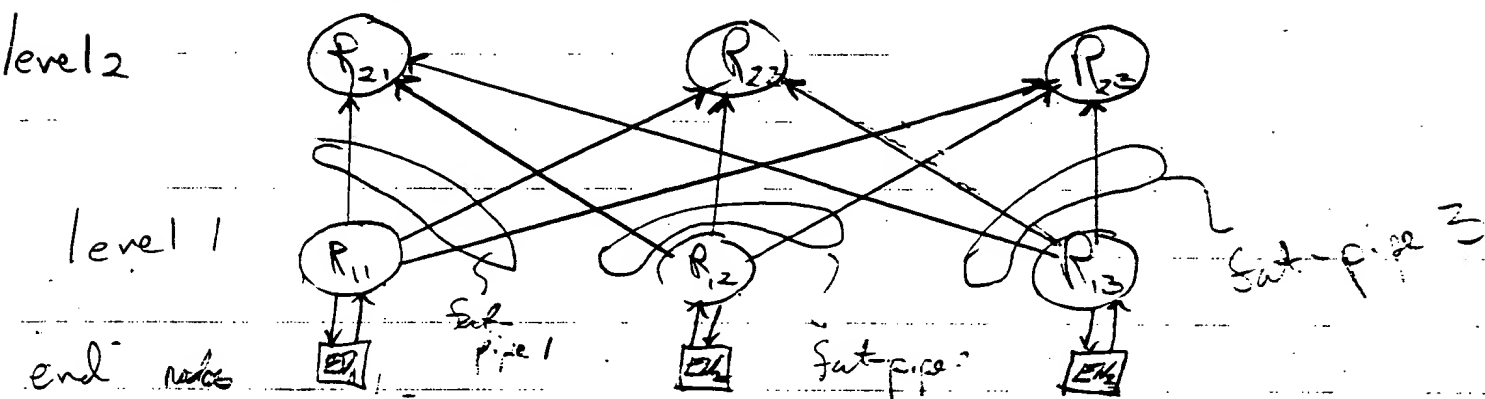


Fig. 11

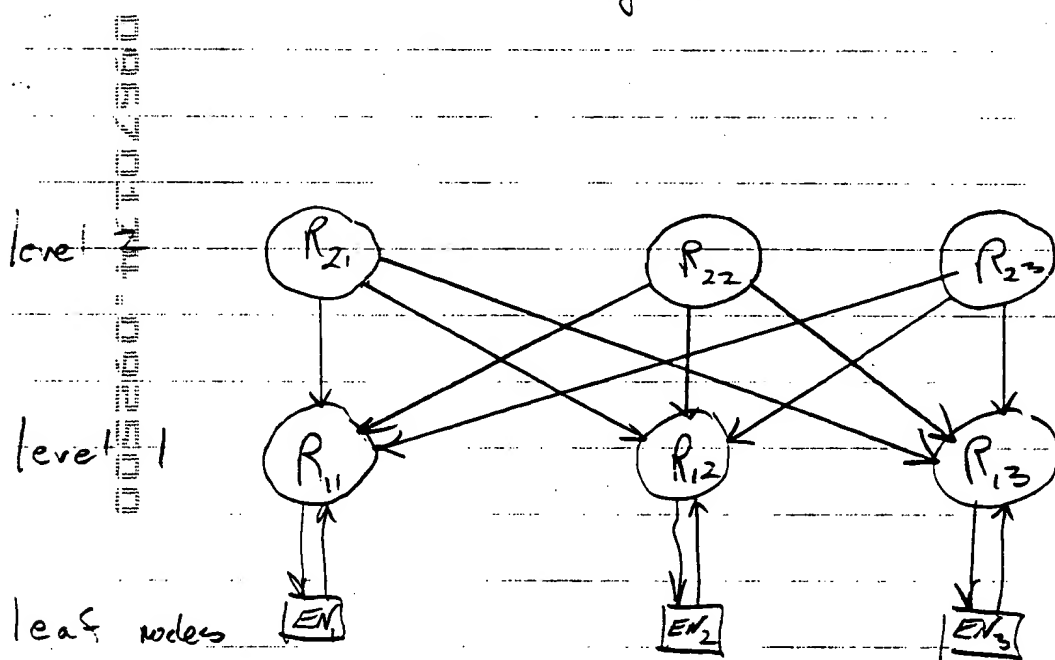


Fig. 12